

A+ Training Guide

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When reviewing corrections, always check the print number of your book. Corrections are made to printed books with each subsequent printing. To determine the print number of your book, view the copyright page. The print number is the right-most number on the line below the "First Printing" line. The corrections in this document apply to the first three printings of this book.

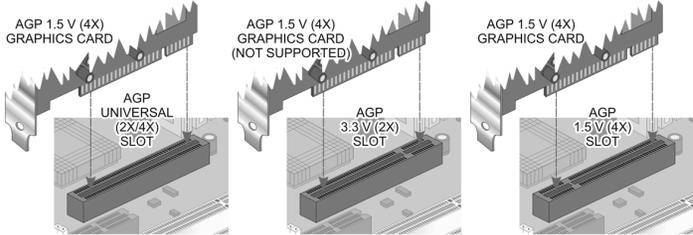
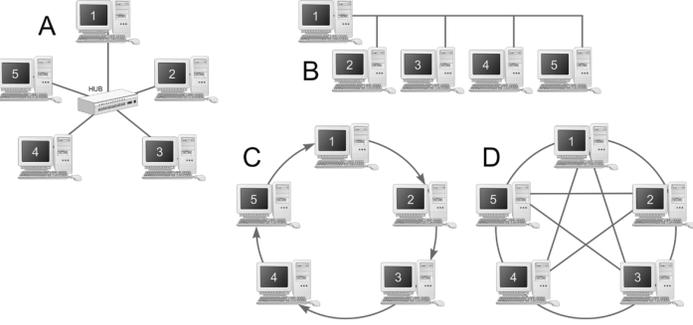
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The following corrections apply to only the first printing of the book.

Pg	Error	Correction
78	Question 20: 20. In Figure 1.28, what type of component is depicted as A?	20. In Figure 1.28, what type of component is depicted as C ?

79	<p>Question 24:</p> <p>24. Which component in Figure 1.29 is considered to be the "brains" of the PC system?</p> <p>A. €</p> <p>B. Ⓓ</p> <p>C. E</p> <p>D. F</p>	<p>24. Which component in Figure 1.29 is considered to be the "brains" of the PC system?</p> <p>A. A</p> <p>B. B</p> <p>C. E</p> <p>D. F</p>
149	<p>Question 22:</p> <p>22. B. In an ATX system...</p>	<p>22. A, B. In an ATX system...</p>
334	<p>Challenge Solutions:</p> <p>2. ...If the system's BIOS doesn't support large LBA or ECHS enhancements, the drive capacity of even the largest hard drive is limited to 528MB...</p>	<p>2. ...If the system's BIOS doesn't support large LBA or ECHS enhancements, the drive capacity of even the largest hard drive is limited to 504MB...</p>

564	Figure 15.10	<p>Replace with this:</p> 
714	Figure 19.17	<p>Replace with this:</p> 
1138	<p>2nd paragraph, 7th line: ...an MBR in Windows 2000/XP (for example, <u>FDSIK</u>, MBR). Although...</p>	<p>...an MBR in Windows 2000/XP (for example, <u>FDSIK</u>, MBR). Although...</p>

The following corrections apply to the first and second printings of the book.

Pg	Error	Correction
79	<p>Question 23:</p> <p>23. In Figure 1.29 the expansion slots are identified as _____.</p> <p>A. B</p> <p>B. C</p> <p>C. D</p> <p>D. E</p>	<p>23. In Figure 1.29 the expansion slots are identified as _____.</p> <p>A. A</p> <p>B. C</p> <p>C. D</p> <p>D. E</p>
446	<p>Delete the 2nd paragraph under the Protecting Monitors section:</p> <p>Aerosol sprays, solvents, and commercial cleaners should be avoided because they can damage the screen and cabinet. The simple cleaning solution, described earlier, is also fine for cleaning the monitor. Make sure that the monitor's power cord is disconnected from any power source before washing. The monitor's screen should be dried with a soft cloth after rinsing.</p>	

456	(The second paragraph under the Uninterruptible Power Supplies section) Uninterruptible power supplies are battery-based systems that monitor the incoming power and kick in when unacceptable variations occur in the power source. The term UPS is frequently used to describe two different types of power backup systems.	Uninterruptible power supplies are battery-based systems that monitor the incoming power and kick in when unacceptable variations occur in the power source as illustrated in Figure 11.6 . The term UPS is frequently used to describe two different types of power backup systems.
456	Last paragraph Both types of UPS systems are depicted in Figure 11.6 .	Both types of UPS systems are depicted in Figure 11.7 .
457	Figure 11.6 UPS systems.	Figure 11.6 A UPS system.
493	Second paragraph, line three: ...execution and 512KB L2 cache features...	...execution and 256KB L2 cache features...
493	End of second paragraph, add a sentence in parenthesis to end of the paragraph.	(The final version of the Pentium II featured a 512KB L2 cache.)
494	1st paragraph, line 3 ...processor (code-named Katmai) was designed around the Pentium II...	...processor (code-named Katmai) was designed around the original Pentium II...
510	Question #3 Add words "the original" to question.	What is the major difference between the original Pentium II and Pentium II microprocessors?

512	<p>Answer #3</p> <p>Add word "original" to sentence.</p>	<p>...code-named Katmai) was designed around the original Pentium II core, but...</p>
844	<p>Question 8</p> <p>8. Which Windows utility was designed specifically to make changes to the Registry in Windows NT, 2000 or XP?</p> <p>A. RegEdit32 B. SysEdit C. RegEdit D. PolEdit</p>	<p>8. Which Windows utility was designed specifically to make changes to the Registry in Windows NT and Windows 2000?</p> <p>A. RegEdt32 B. SysEdit C. RegEdit D. PolEdit</p>
849	<p>8. A. Windows 2000 includes two Registry Editors: RegEdit and RegEdt32. Both utilities enable you to add, edit, and remove Registry entries and to perform other basic functions; however, specific functions can be performed only in one editor or the other. RegEdt32 is the Registry Editor that is used with Windows NT/2000. RegEdit is the Registry Editor that was introduced with Windows 95.</p>	<p>8. A. Windows NT and Windows 2000 include two Registry Editors: RegEdit and RegEdt32. Both utilities enable you to add, edit, and remove Registry entries and to perform other basic functions; however, specific functions can be performed only in one editor or the other. RegEdt32 is designed specifically to manage the Windows NT/2000 Registry. RegEdit was introduced with Windows 95.</p>

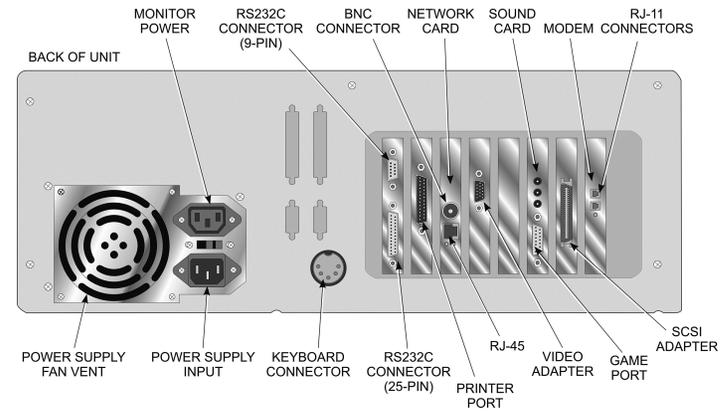
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199	Replace Table 4.1	<p>with:</p> <table border="1" data-bbox="1087 394 1774 722"> <thead> <tr> <th>INTERRUPT</th> <th>DESCRIPTION</th> <th>INTERRUPT</th> <th>DESCRIPTION</th> </tr> </thead> <tbody> <tr> <td>NMI</td> <td>I/O CHANNEL CHECK OR PARITY CHECK ERROR</td> <td></td> <td></td> </tr> <tr> <td colspan="2">INTC1</td> <td colspan="2">INTC2</td> </tr> <tr> <td>IRQ0</td> <td>TIMER/COUNTER ALARM</td> <td>IRQ8</td> <td>REAL-TIME CLOCK</td> </tr> <tr> <td>IRQ1</td> <td>KEYBOARD BUFFER FULL</td> <td>IRQ9</td> <td>SPARE</td> </tr> <tr> <td>IRQ2</td> <td>CASCADE FROM INTC2</td> <td>IRQ10</td> <td>SPARE</td> </tr> <tr> <td>IRQ3</td> <td>SERIAL PORT 2</td> <td>IRQ11</td> <td>SPARE</td> </tr> <tr> <td>IRQ4</td> <td>SERIAL PORT 1</td> <td>IRQ12</td> <td>SPARE PS/2 MOUSE</td> </tr> <tr> <td>IRQ5</td> <td>PARALLEL PORT 2</td> <td>IRQ13</td> <td>COPROCESSOR</td> </tr> <tr> <td>IRQ6</td> <td>FDD CONTROLLER</td> <td>IRQ14</td> <td>PRIMARY IDE CTRL</td> </tr> <tr> <td>IRQ7</td> <td>PARALLEL PORT 1</td> <td>IRQ15</td> <td>SECONDARY IDE CTRL</td> </tr> </tbody> </table>	INTERRUPT	DESCRIPTION	INTERRUPT	DESCRIPTION	NMI	I/O CHANNEL CHECK OR PARITY CHECK ERROR			INTC1		INTC2		IRQ0	TIMER/COUNTER ALARM	IRQ8	REAL-TIME CLOCK	IRQ1	KEYBOARD BUFFER FULL	IRQ9	SPARE	IRQ2	CASCADE FROM INTC2	IRQ10	SPARE	IRQ3	SERIAL PORT 2	IRQ11	SPARE	IRQ4	SERIAL PORT 1	IRQ12	SPARE PS/2 MOUSE	IRQ5	PARALLEL PORT 2	IRQ13	COPROCESSOR	IRQ6	FDD CONTROLLER	IRQ14	PRIMARY IDE CTRL	IRQ7	PARALLEL PORT 1	IRQ15	SECONDARY IDE CTRL
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Replace Figure 5.2

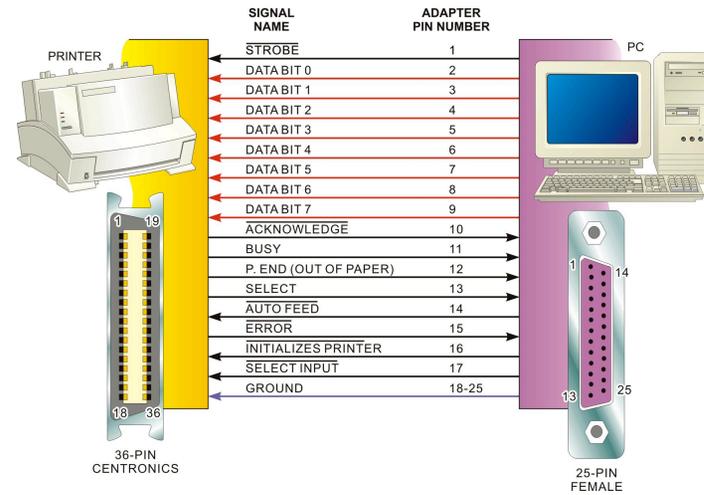
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230

Replace Figure 5.4

with:



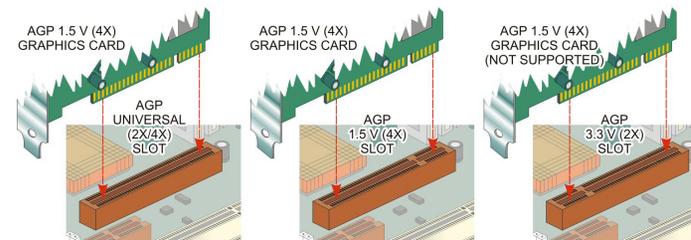
508 Replace Table 13.4

with:

MICROPROCESSOR	DIAMETER SIZE (mm)	VRM (VOLTS)	SPEED (MHz)	CACHE ON DIE (kB)	CACHE ON CARTRIDGE	CACHE ON BOARD (kB)	SOCKET'S OR SLOT TYPES
Pentium	23.1x23.1	2.5-3.6	75-166	L1-8kB	-	L2-256/512	Socket 7
Pentium MMX	25.4x25.4	2.0-3.5	166-233	L1-16+16	-	L2-256/512	Socket 7
AMD - K6-2/K6-3	33.5x33.5	2.2-3.3	300-550	L1-32+32	-	L2-256/512	Super Socket 7
Pentium Pro	24.2x19.6	3.1-3.3	150, 166, 180, 200	L1-8kB	L2-256/512/1000	-	Socket 8
Pentium IV/III Celeron (25 micron)	25.4x25.4 18x6.2x140 Box	1.5-2.6	233-1000	L1-16+16	L2-256/512 128 kB	-	Slot 1
Xeon IV/III (330 (25 micron)	27.4x27.4 18x6.2x125 Box	1.5-2.6	500/550 700/600	L1-16+16	L2-612 kB 1 MB 2 M	-	Slot 2
Pentium III Celeron (25 micron)	25.4x25.4 Slug 27.4x27.4 Opening	1.1-2.5	300-566	L1-16+16 L2-128/256	-	-	Socket 370 PPGA
Pentium III Celeron (Coppermine) Celeron (18 micron)	9.2x11.3	1.1-2.5	667-1000	L1-16+16 L2-128/256	-	-	Socket 370 FC-PGA
Pentium III (Tualatin) Celeron (13 micron)	31x31	1.1-2.5	800-1500	L1-16+16 L2-128/256/512	-	-	FC-PGA2
Pentium IV (18 micron)	31x31	1.75	1300-2000	L1-12+8 L2-256	-	-	Socket 423 FC-PGA
Pentium IV (13 micron)	31x31 35x33	1.75 1.50	1400-2000 1800-3400	L1-12+8 L2-612	-	-	FC-PGA2
Pentium Xeon (18 micron)	31x31	1.4-1.8 1.7	1400-2000	L1-12+8 L2-256	-	-	Socket 603 FC-BGA
Pentium Xeon (13 micron)	35x35	1.4-1.8 1.475	1800-3400	L1-12+8 L2-612	-	-	Socket 603 FC-BGA2
Itanium (18 micron) (266 MHz)	71.6x127.7	1.7	733/600	L1-16+16 L2-612	L3-2 MB 4 MB	-	PAC-418
Athlon Duron	9.1x13.1	1.75	800-1400	L1-64+64	L2-256 kB	-	Slot A242 CPGA
Athlon Duron	11.1x11.0	1.75	733-1800 1400-3200	L1-64+64	L2-256	-	Socket A462 OPGA

564 Replace Figure 15.10

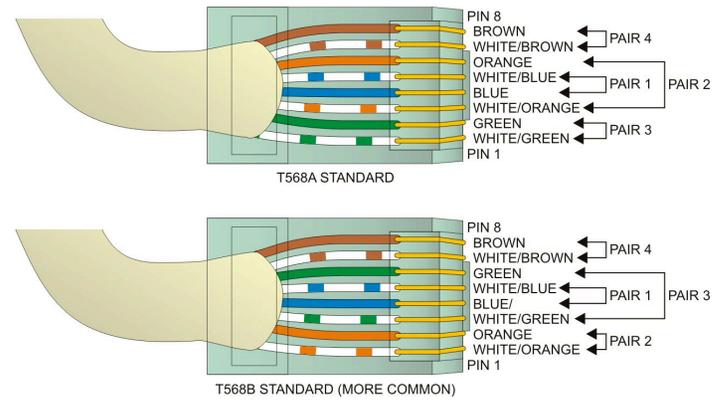
with:



727

Replace Figure 20.2

with:



This errata sheet is intended to provide updated technical information. Spelling and grammar misprints are updated during the reprint process, but are not listed on this errata sheet.