

ERRATA

Page 54 – Table 2-1: Fourth row under heading High-Order Bits (Hex), should read “**FE80::/10**” instead of “FF80::/10”.
Fifth row under heading High-Order Bits (Hex), should read “**FEC0::/10**” instead of “FFC0::/10”.

Page 55 – Under heading Local Unicast Addresses
Second paragraph, last sentence should read “**FE80::/10**” instead of “FF80::/10”
Last paragraph should read “**FEC0::/10**” instead of “FFC0::/10”

Page 72 – Last paragraph, second sentence should read “**0xFE80::/10**” instead of “0xFF80::/10”.
Last paragraph, last sentence should read “**0xFE80::/10**” instead of “0xFF80::/10”.

Page 206 – Under heading Classless Routing Protocols
Second paragraph, last sentence should read “**172.16.255.255/16**” instead of “172.168.255.255/16”

Page 536 – Figure 10-9 should be replaced figure below:
Figure is not an analyzer capture IS-IS PDU and the diagram given is OSPFv3 Inter-Area Router LSA.

