A+ Exam Cram 2

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First Printing Corrections

Pg	Error	Correction
21	First full sentence: Figure 2.5 demonstrates this change, which then carried forward to the ~~**LPX**~~ and NLX form factors.	First full sentence: Figure 2.5 demonstrates this change, which then carried forward to the **ATX** and NLX form factors.
35	Second sentence under Slot Technology: Pentium II, Pentium III, the Celeron, and AMD's Athlon chip are all provided in SEC or SEP packaging.	Second sentence under Slot Technology: Pentium II, Pentium III, **Pentium** Xeon, the Celeron, and AMD's Athlon chip are all provided in SEC or SEP packaging.
36	Fifth sentence under Note: Slot A was used for the Athlon ~~**and Duron**~~ processors, and was incompatible with Slot 1 or 2.	Fifth sentence under Note: Slot A was used for the Athlon processors, and was incompatible with Slot 1 or 2.
42	First full paragraph, second sentence: Like AGP X4, it implements a 32-bit wide bus, but the new specification allows for double the previous speed, to 533 MHz, and supports a data rate of ~~**two gigabytes per second (2 GB/s)**~~**.**	First full paragraph, second sentence: Like AGP X4, it implements a 32-bit wide bus, but the new specification allows for double the previous speed, to 533 MHz, and supports a data rate of **2.1 gigabytes per second (2.1 GB/s).**
95	Fourth full paragraph, first sentence: With built-in memory management, the 386-DX enabled software to access 64 terabytes (TB) of memory, or about ~~**1**~~ trillion bytes, so software written for the 386 chip could access 64 trillion bytes of memory.	Fourth full paragraph, first sentence: With built-in memory management, the 386-DX enabled software to access 64 terabytes (TB) of memory, or about **64** trillion bytes, so software written for the 386 chip could access 64 trillion bytes of memory.

104	Second paragraph under Inter Celeron, first sentence: Celeron processor packaging ~~**is generally**~~ found in one of ~~**two**~~ formats: a PGA 370 socket (Socket 370) ~~**or**~~ FC-PGA.	Second paragraph under Inter Celeron, first sentence: Celeron processor packaging **can be** found in one of **three** formats: a PGA 370 socket (Socket 370), FC-PGA, **and socket 470, the latest version**.
104	Second bullet in Intel Celeron: The multipliers ranged from 4 through 9, generating chip speeds of ~~**266MHz, 300MHz, 333MHz, 366MHz, 400MHz, 433MHz, 466MHz, 500MHz, 533MHz, 566MHz, and 600MHz**~~.	Second bullet in Intel Celeron: The multipliers ranged from 4 through 9, generating chip speeds of **up to 2.4GHz**.
154	Question 2 (explanation paragraph) Answers a, c, and d are correct. Foam element, ~~**capacitive**~~, and rubber dome keyboards are considered mechanical.	Question 2 (explanation paragraph) Answers a, c, and d are correct. Foam element, **membrane**, and rubber dome keyboards are considered mechanical.
208	(bulleted list) AGPX4-66MHz clock, ~~**066MB**~~/s throughout	(bulleted list) AGPX4-66MHz clock, **1.07GB**/s throughout
270	First paragraph after HTML and XML sidebar: In the URL **http:www.jamesgjones.com**, the James G~~.~~ Jones domain is a *com*mercial domain and there has a com suffix.	First paragraph after HTML and XML sidebar: In the URL **http:www.jamesgjones.com**, the James G Jones domain is a *com*mercial domain and there has a com suffix.
300	(second paragraph below "Wireless Networking," last sentence) Speeds range from .9 to 1.1 ~~**Mpbs**~~.	(second paragraph below "Wireless Networking," last sentence) Speeds range from .9 to 1.1 **Mbps**.
343	Additional information following the last paragraph…	*Partitioning and Multiple Physical Disks* We were asked if, on a multi-disk system, a physical disk could be partitioned as one, single

		extended partition. Initially, we thought that a disk must have at least one Primary partition in order for the operating system to recognize the structural organization. In fact, and working with various operating systems on multi-disk systems, we found that a second physical disk can be partitioned as a single extended partition. Obviously, one disk in the system must have a Primary, Active partition in order to boot the OS, but secondary disks or any additional disks may be set up entirely as extended partitions. The additional disks can then be logically formatted into various drives, and assigned drive letters. When multiple Primary partitions have been created, along with extended, logical drive partitions, MS Explorer will present the Primary partitions first, in the Folders view (left pane), in the order in which they're found on the drive controllers. Non-Primary drives are listed following (below) the Primary partitions, also in the order of the drive controllers. For example, a two-disk system with a Primary partition on both disks and an Extended partition on each, will show as C: for Disk 0 (Primary), and D: for Disk 1 (Primary). Explorer will then show Drive E: on Disk 0, and finally, Drive F: on Disk 1. This can lead to some confusion if you install a second fixed disk and decide to include on it both a Primary and Extended partition (for example if you wanted two separate operating systems, separated by physical disks). In this case, drive references will

		likely change if the Explorer can see every disk. We tested this on both Windows NT (XP) and Windows 9x systems.
574	Last paragraph, first sentence: XP~~**, like Win2000,**~~ comes in two versions: the Home and the Professional editions.	Last paragraph, first sentence: XP comes in two versions: the Home and the Professional editions.
659	54. ~~**d**~~	54. **c**

Page 135, corrected Table 5.1:

DMA Channel	*Bus Width*	*Default Device*
0	NA	DRAM refresh
1	8- or 16-bit	Sound cards (low)
2	8- or 16-bit	Floppy disks
3	8- or 16-bit	Not assigned
4	NA	Cascade to DMA 5-7
5	16-bit	Sound cards (high)
6	16-bit	Not assigned
7	16	Not assigned

Page 649, corrected figure 15.1: